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41. (Amended) A device according to claim 32 wherein said semiconductor layer has an electron mobility of 15 to 300 cm<sup>2</sup>/Vsec.

E2

42. (Amended) A device according to claim 32 wherein said semiconductor layer has a hole mobility of 10 to 200 cm<sup>2</sup>/Vsec.

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54. (Amended) A device according to claim 45 wherein said semiconductor layer has an electron mobility of 15 to 300 cm<sup>2</sup>/Vsec.

E3

55. (Amended) A device according to claim 45 wherein said semiconductor layer has a hole mobility of 10 to 200 cm<sup>2</sup>/Vsec.

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Please add new claims 77-80 as follows.

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77. A semiconductor device according to claim 1, wherein said semiconductor layer has an electron mobility of 15 to 300 cm<sup>2</sup>/Vsec.

E4

78. A semiconductor device according to claim 1, wherein said semiconductor layer has a hole mobility of 10 to 200 cm<sup>2</sup>/Vsec.

79. A semiconductor device according to claim 10, wherein said semiconductor layer has an electron mobility of 15 to 300 cm<sup>2</sup>/Vsec.

80. A semiconductor device according to claim 10, wherein said semiconductor layer has a hole mobility of 10 to 200 cm<sup>2</sup>/Vsec.

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